

FIG. 1

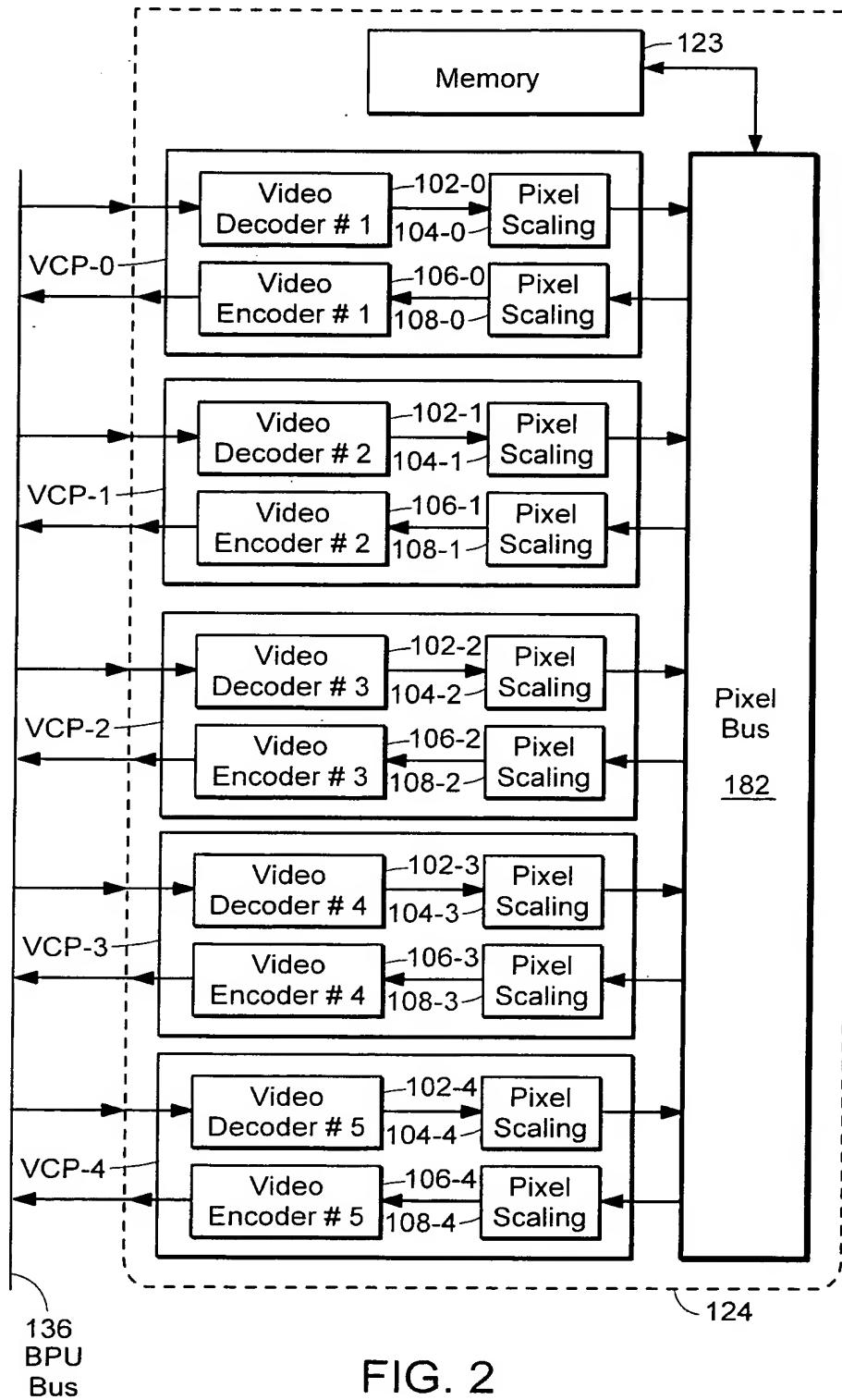


FIG. 2

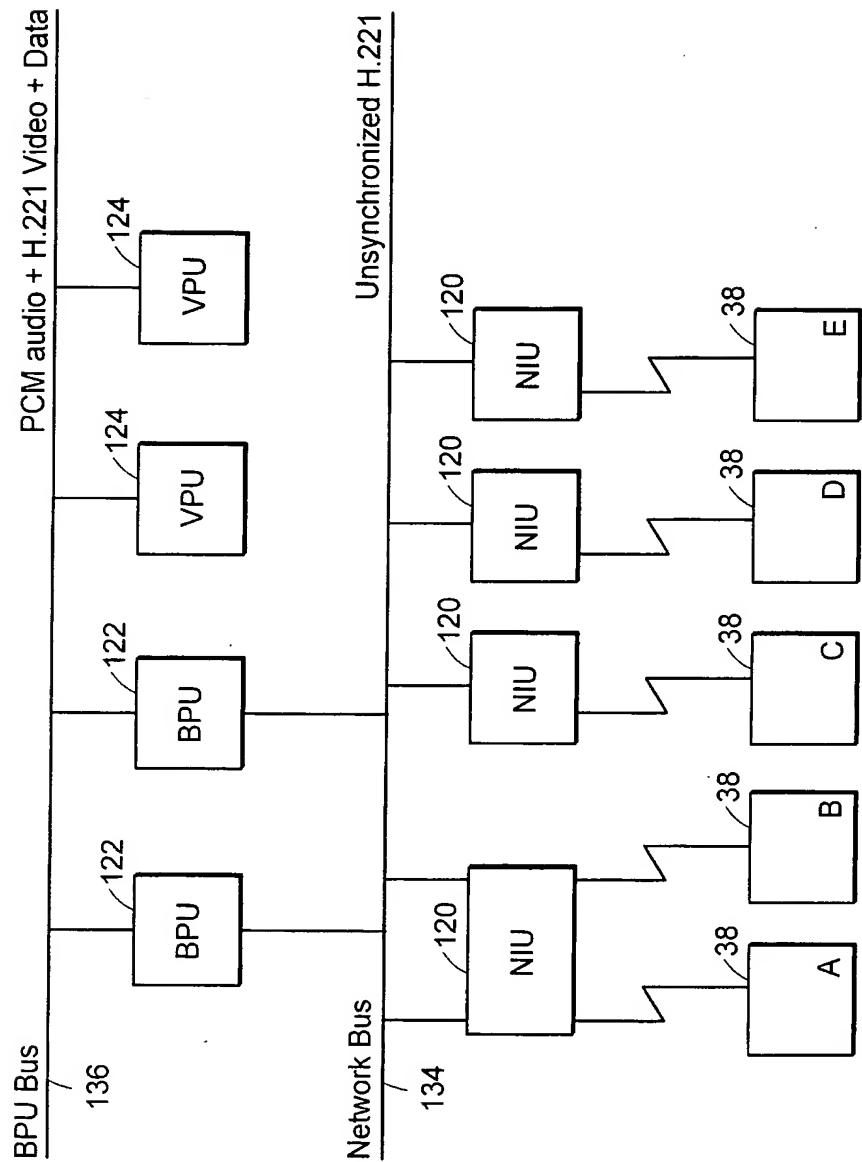


FIG. 3

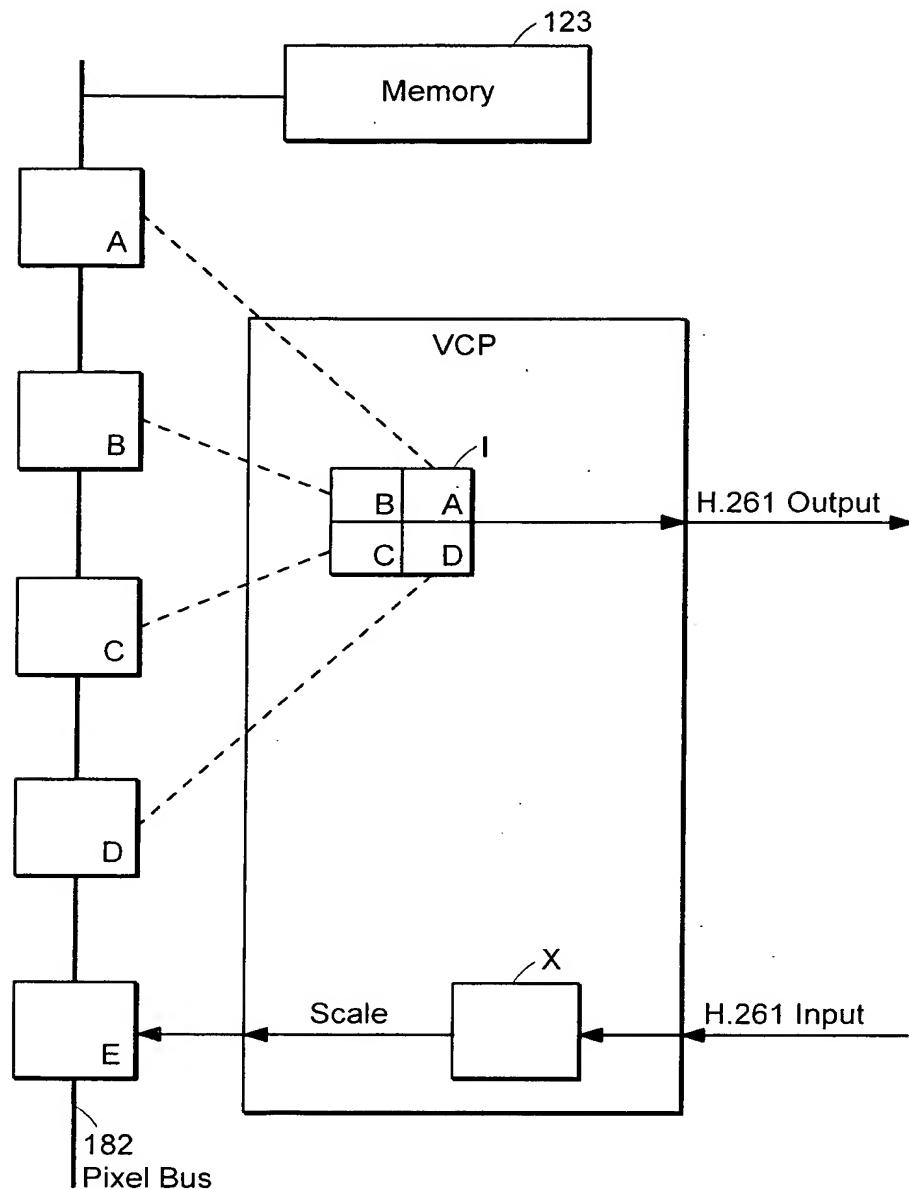


FIG. 4

5/12

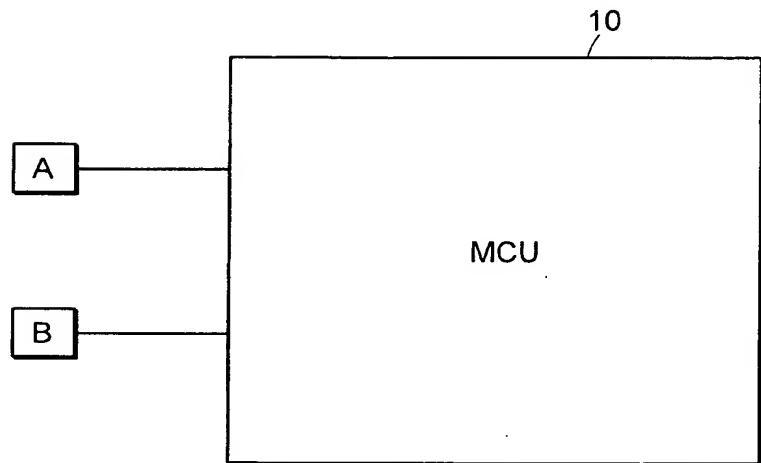


FIG. 5A

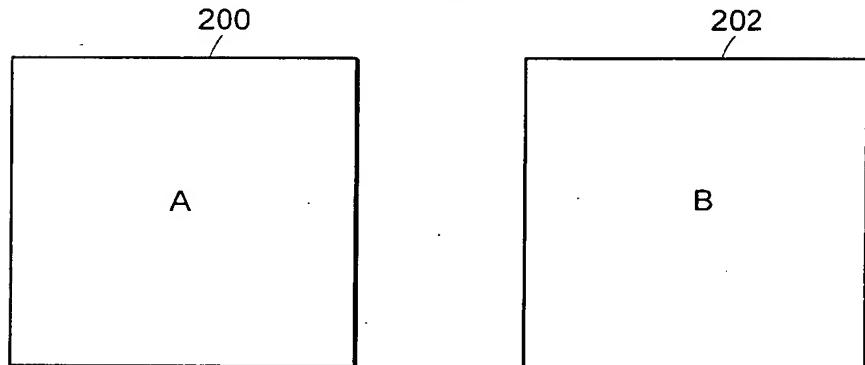


FIG. 5B

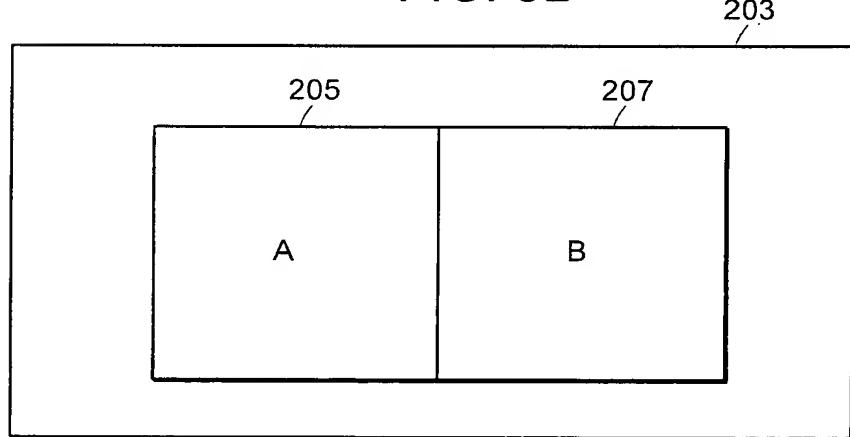


FIG. 5C



Docket/Appl'n No.: 10/601,095
Title: Method and Apparatus for Video....
Inventors: Tom Erik Lia, et al.

Replacement Sheet

6/12

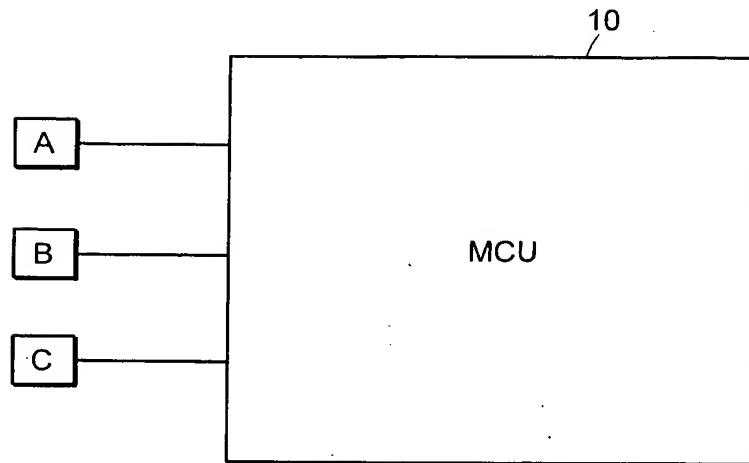


FIG. 6A

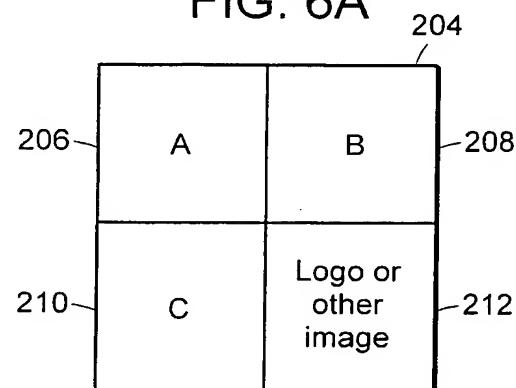


FIG. 6B

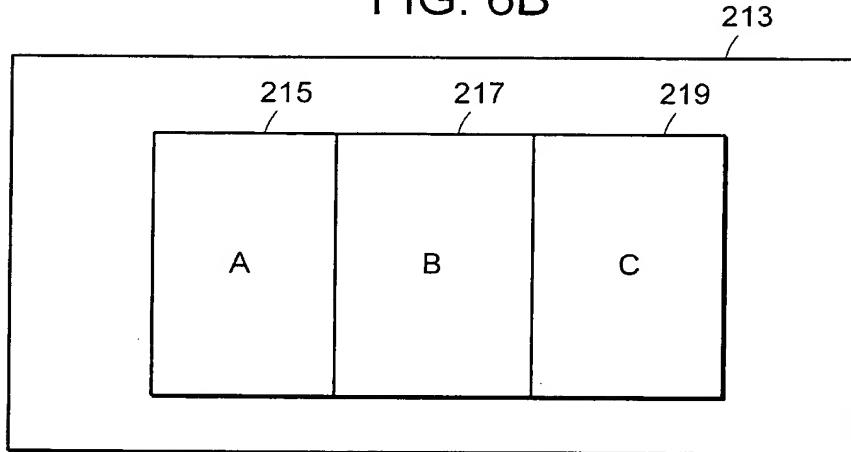


FIG. 6C



7/12

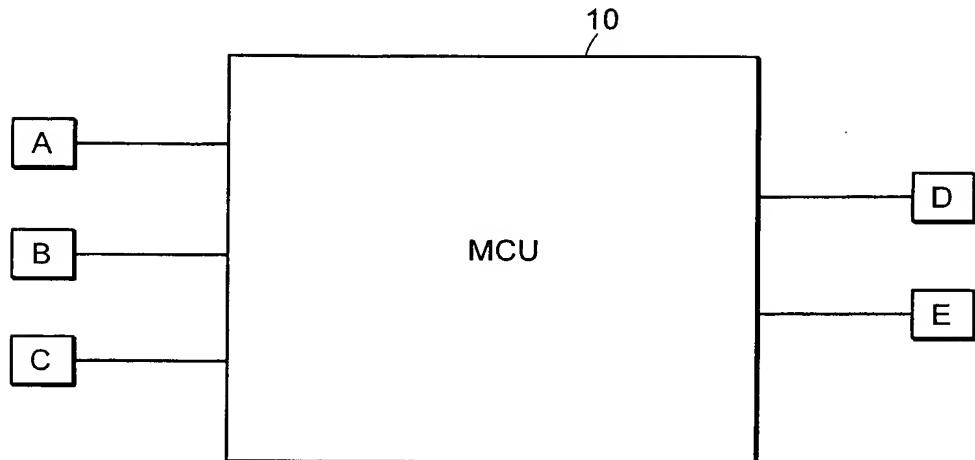


FIG. 7A

A	B	C
<u>222</u>	<u>224</u>	<u>226</u>
D	E	Logo or other image <u>232</u>
Logo or other image <u>234</u>	Logo or other image <u>236</u>	Logo or other image <u>238</u>

FIG. 7B

A	B	C
<u>225</u>	<u>227</u>	<u>229</u>
D	E	Logo or other image <u>235</u>

223

FIG. 7C



Docket/Appl'n No.: 10/601,095
Title: Method and Apparatus for Video....
Inventors: Tom Erik Lia, et al.
Replacement Sheet

8/12

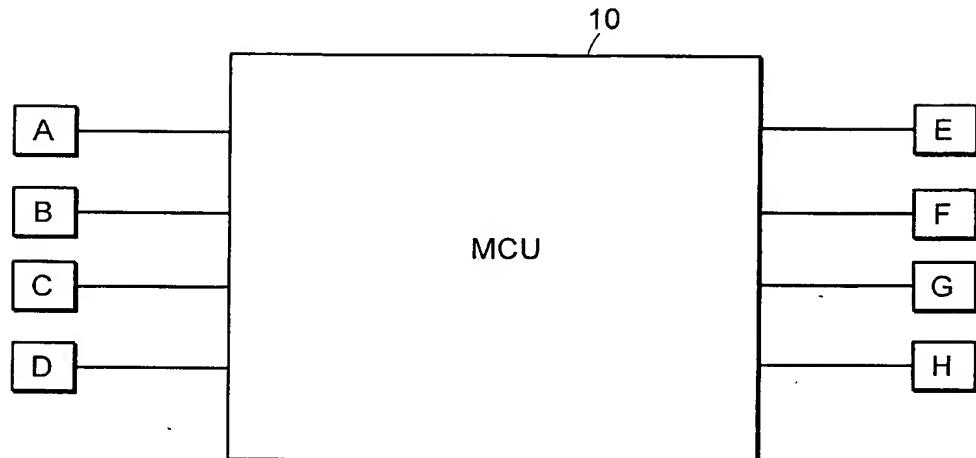


FIG. 8A

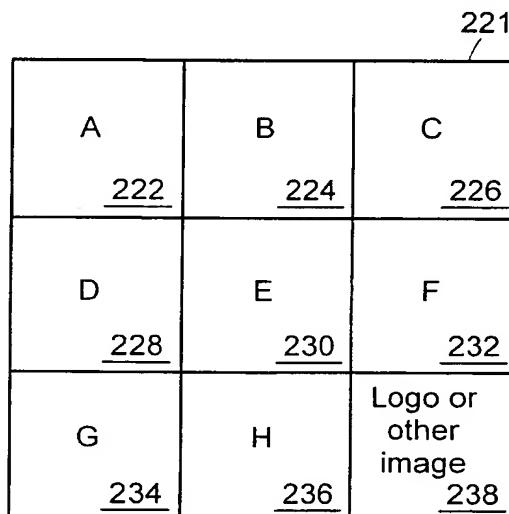


FIG. 8B



Docket/Appl'n No.: 10/601,095
Title: Method and Apparatus for Video....
Inventors: Tom Erik Lia, et al.

Replacement Sheet

9/12

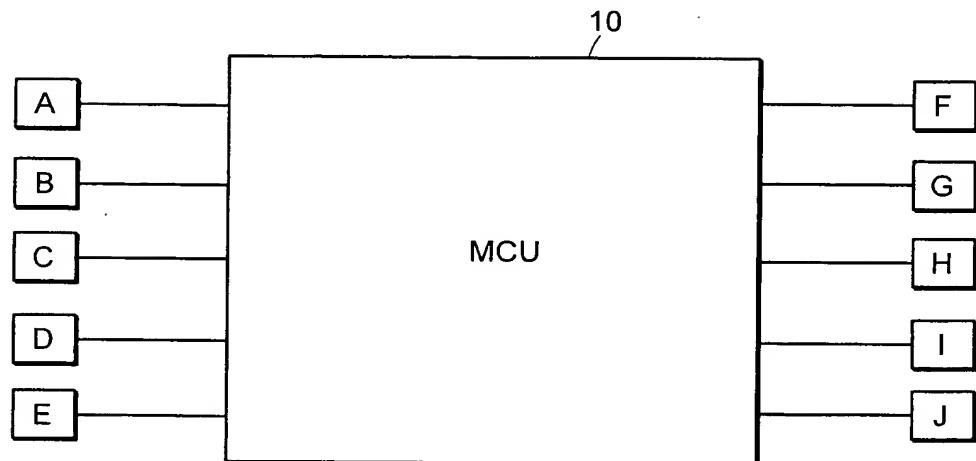


FIG. 9A

Diagram showing a detailed connection matrix between the inputs and outputs of the MCU. The matrix is a 4x4 grid with a header row and a reference row '240' above it.

240			
A 242	B 244	C 246	D 248
E 250	F 252	G 254	H 256
I 258	J 260	Logo or other image 262	Logo or other image 264
Logo or other image 266	Logo or other image 268	Logo or other image 270	Logo or other image 272

FIG. 9B



Docket/Appl'n No.: 10/601,095
Title: Method and Apparatus for Video....
Inventors: Tom Erik Lia, et al.

Replacement Sheet

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10/12

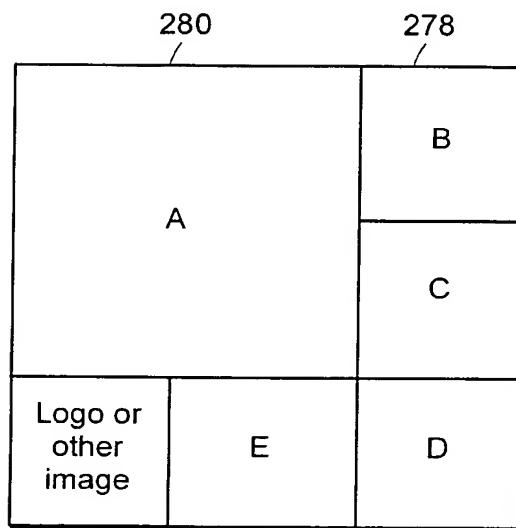


FIG. 10



11/12

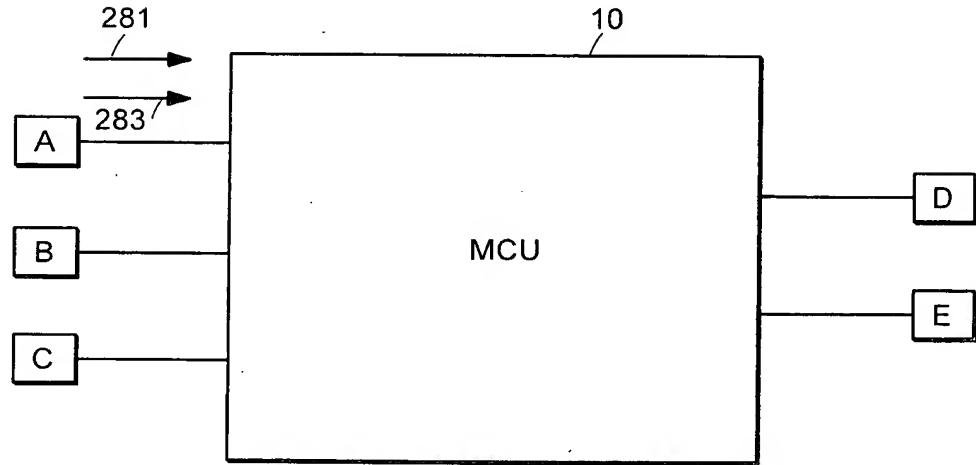


FIG. 11A

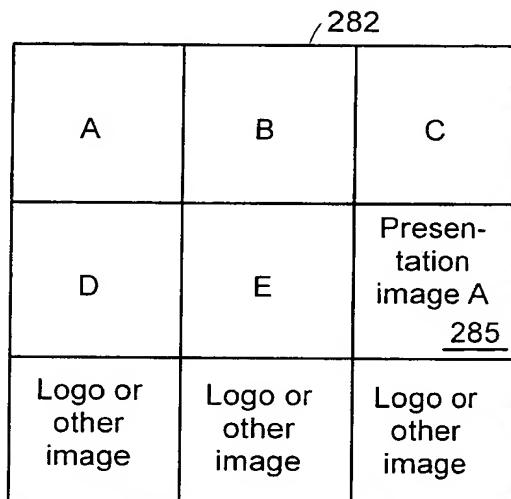


FIG. 11B

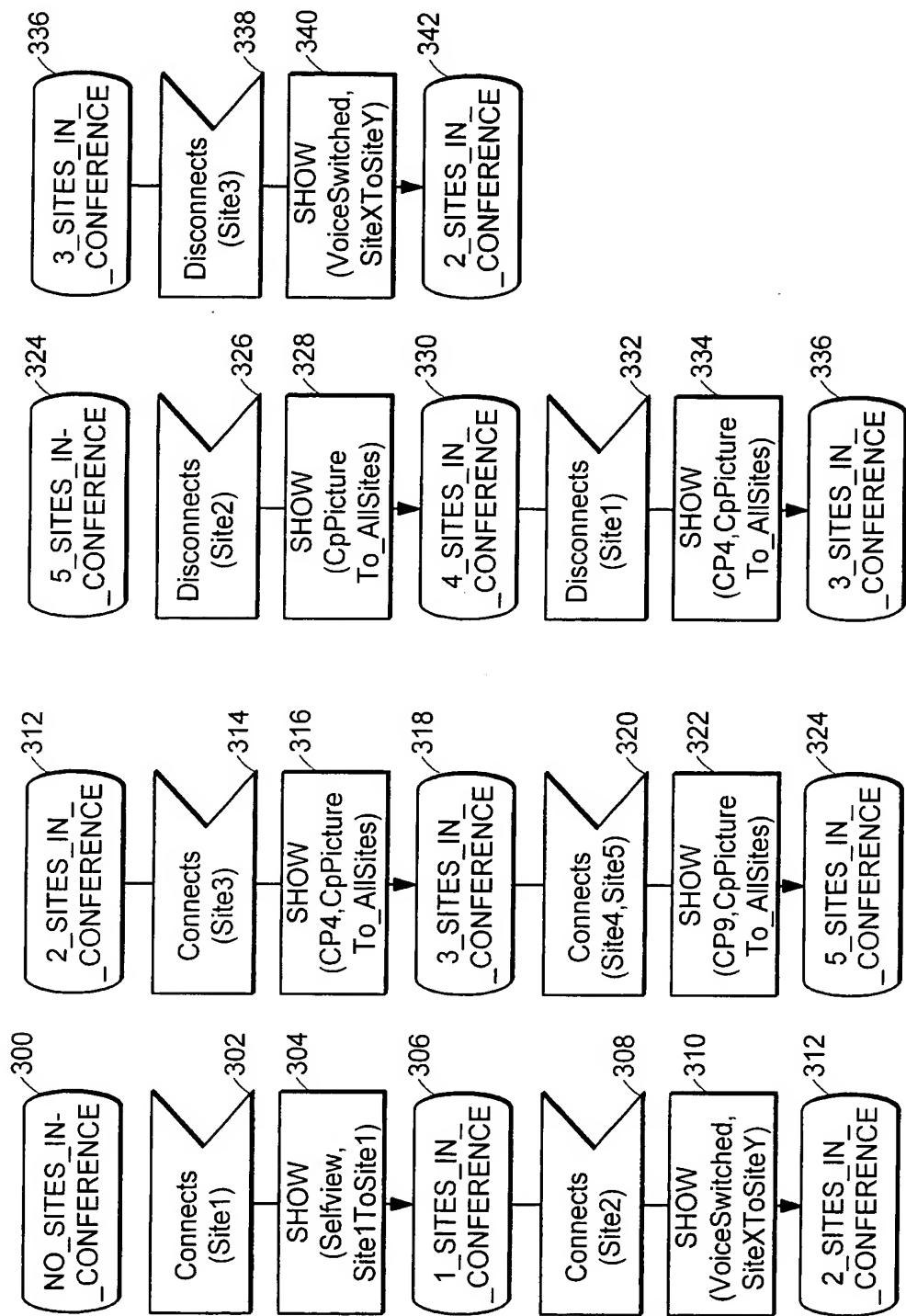


FIG. 12